Manufacturing RV64I CPUs, boot
ROMS/firmware (mask ROM/diode matrix),
punch cards, memory mapped IO for LEDs,
volatile RAM (SRAM and DRAM) and
micro-architecture including cache, prefetching,
instruction pipelines, branch prediction,
out-of-order execution and speculative execution

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Part I Introduction

Part II Manufacturing CPUs

$\begin{array}{c} {\rm Part~III} \\ {\rm RAM~and~storage} \end{array}$

$\begin{array}{c} {\rm Part~IV} \\ {\rm Motherboards} \end{array}$

Part V

Keyboards, printers and terminals